

SPB18P06P G-VB Datasheet

P-Channel 60-V (D-S) MOSFET

PRODUC	T SUMMARY		
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)	Q _g (Typ)
- 60	0.064 at V _{GS} = - 10 V	- 30	12
- 00	0.077 at V _{GS} = - 4.5 V	- 28	12

FEATURES

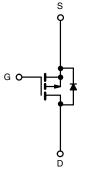
- Trench Power MOSFET100
- % UIS Tested

APPLICATIONS

Load Switch







P-Channel MOSFET

Parameter		Symbol	Limit	Unit
Gate-Source Voltage		V _{GS}	± 20	V
Continuous Drain Current ($T_1 = 175 ^{\circ}C$)	T _C = 25 °C	1-	- 30	
O(R) and $O(R)$ $O(R)$ $O(R)$ $O(R)$ $O(R)$	T _C = 100 °C	. I _D	- 20	1
Pulsed Drain Current	·	I _{DM}	- 90	А
Continuing Source Current (Diode Conduction)		۱ _S	- 28	
Avalanche Current		I _{AS}	- 31	
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	7.2	mJ
Maximum Dawar Dissinction	T _C = 25 °C	Р	60 ^a	w
Maximum Power Dissipation	T _A = 25 °C	P _D	6 ^b	vv
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
hunding to Ambient	$t \le 10 \text{ sec}$	R _{thJA}	20	25	
Junction-to-Ambient ^D	Steady State	' 'thJA	62	75	°C/W
Junction-to-Case		R _{thJC}	5	6	

Notes:

a. See SOA curve for voltage derating.

b. Surface Mounted on 1" x 1" FR-4 boad.

SPECIFICATIONS $T_J = 25$	°C, unless	otherwise noted				
Parameter	Symbol	Test Conditions	Min	Typ ^a	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 V, I_D = -250 \mu A$	- 60			v
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.0	- 2.0	- 3.0	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
		$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = - 60 V, V_{GS} = 0 V, T_{J} = 125 °C			- 50	μΑ
		$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$			- 150	
On-State Drain Current ^b	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 10			А
		V _{GS} = - 10 V, I _D = - 5 A		0.064		
		V_{GS} = - 10 V, I_D = - 5 A, T_J = 125 °C		0.110		
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = - 10 V, I _D = - 5 A, T _J = 175 °C		0.250		Ω
		V _{GS} = - 4.5 V, I _D = - 2 A		0.077		1
Forward Transconductance ^b	9 _{fs}	V _{DS} = - 15 V, I _D = - 5 A		8		S
Dynamic	•	• • •		•		•
Input Capacitance	C _{iss}			1000		
Output Capacitance	C _{oss}	V_{DS} = - 25 V, V_{GS} = 0 V, f = 1 MHz		210		pF
Reverse Transfer Capacitance	C _{rss}			110		
Total Gate Charge	Qg			12.5	19	
Gate-Source Charge	Q _{gs}	$V_{DS} = -30$ V, $V_{GS} = -10$ V, $I_{D} = -8.4$ A		2.3		nC
Gate-Drain Charge	Q _{gd}			3.2		1
Gate Resistance	R _g	f = 1 MHz		8.0		Ω
Turn-On Delay Time ^c	t _{d(on)}			5	10	
Rise Time ^c	t _r	V_{DD} = - 30 V, R_L = 3.57 Ω		14	25	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ - 8.4 A, V_{GEN} = - 10 V, R_G = 2.5 Ω		15	25	ns
Fall Time ^c	t _f	1		7	12	
Source-Drain Diode Ratings and Cha	aracteristics	(T _C = 25 °C) ^b				
Pulsed Current	I _{SM}				- 30	А
Forward Voltage ^b	V _{SD}	$I_{F} = -2 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.9	- 1.3	V
Reverse Recovery Time	t _{rr}	I _F = - 8 A, di/dt = 100 A/μs		50	80	ns
Reverse Recovery Time	Q _{rr}	$F = -0 A, ui/ul = 100 A/\mu S$		80	120	nC

Notes:

a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

c. Independent of operating temperature.

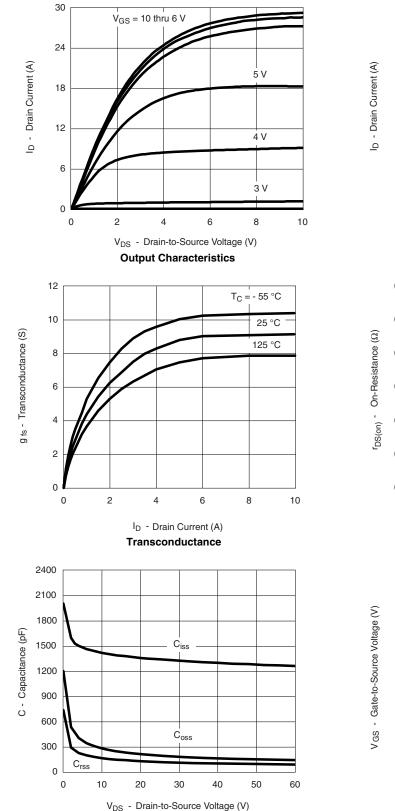
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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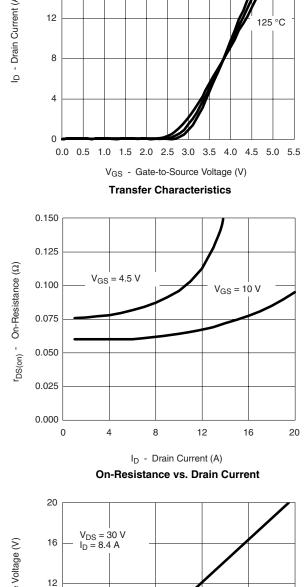
T_C = - 55 °C

1 25 °C



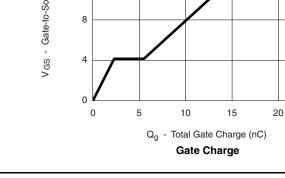
Capacitance

TYPICAL CHARACTERISTICS 25 °C unless noted



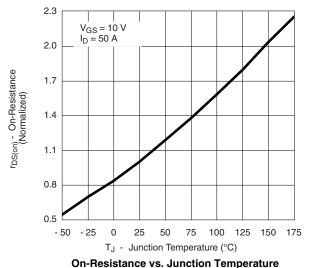
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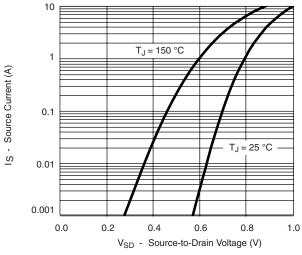
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25

TYPICAL CHARACTERISTICS 25 °C unless noted

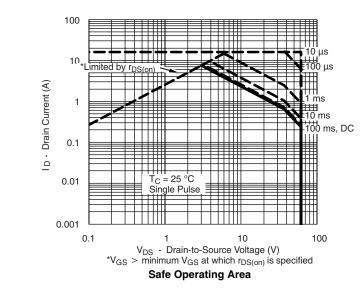




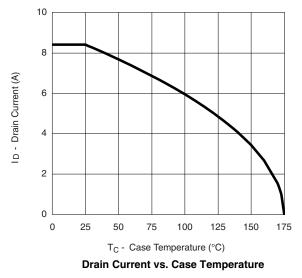
Source-Drain Diode Forward Voltage

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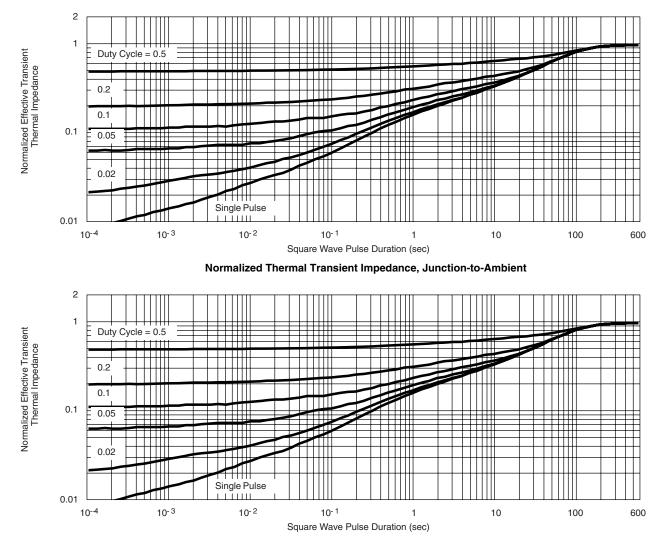


THERMAL RATINGS





THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case

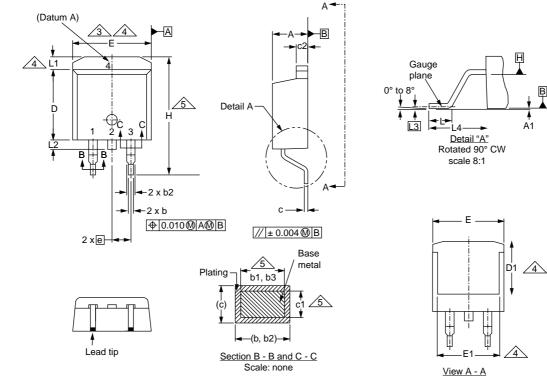
SPB18P06P G-VB



B

Seating plane

TO-263AB



	MILLI	METERS	INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.06	4.83	0.160	0.190
A1	0.00	0.25	0.000	0.010
b	0.51	0.99	0.020	0.039
b1	0.51	0.89	0.020	0.035
b2	1.14	1.78	0.045	0.070
b3	1.14	1.73	0.045	0.068
С	0.38	0.74	0.015	0.029
c1	0.38	0.58	0.015	0.023
c2	1.14	1.65	0.045	0.065
D	8.38	9.65	0.330	0.380
CN: S-82 WG: 597	110-Rev. A,	15-Sep-08		-

Notes

1. Dimensioning and tolerancing per ASME Y14.5M-1994.

2. Dimensions are shown in millimeters (inches).

3. Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body at datum A.

4. Thermal PAD contour optional within dimension E, L1, D1 and E1.

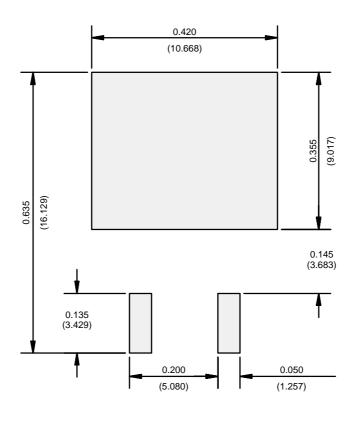
5. Dimension b1 and c1 apply to base metal only.

6. Datum A and B to be determined at datum plane H.

7. Outline conforms to JEDEC outline to TO-263AB.



RECOMMENDED MINIMUM PADS FOR D²PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)



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